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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/811,551

03/29/2004

Dean T. Lindsay

42P18576

1393

8791

7590

11/23/2005

EXAMINER

DALEY, CHRISTOPHER ANTHONY

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ART UNIT

PAPER NUMBER

2111

DATE MAILED: 11/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No. 10/811,551	Applicant(s) LINDSAY, DEAN T.	
	Examiner Christopher A. Daley	Art Unit 2111	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 29 March 2004.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4, 11, 12 and 15-18 is/are rejected.
- 7) ☒ Claim(s) 5-10, 13, 14 and 19-24 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No./\_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)  | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### DETAILED ACTION

Claims 1 – 24 are pending.

#### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 –4, 11-12, and 15 – 18 are rejected under 35 U.S.C. 102(b) as being anticipated by Chemla (US5805403).

As to claims 1 and 11, Chemla discloses a system and method comprising:

a first integrated circuit (IC) (Figure 4 shows a first integrated circuit processor 18, COL. 5, lines 50- 15);

an interface coupled to the first IC (Said integrated circuit has a interface coupled to itself in the embodiment of temperature sensors of IC 50. Said interface is the JTAG interface shown in figure 5);

and a second IC coupled to the interface, wherein the first IC simultaneously transmits the state of each of a plurality of signals not associated with the interface to the second IC in-band via the interface each time that a change in the state of one of the plurality of signals is detected (When an overheat condition is detected on ICs 50, output signal to the processor and other ICs that a temperature

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overheat situation has occurred, COL. 4, lines 55 – 67. This information is sent to the second IC , processor 18 of figure 5).

As to claims 2, 12, and 16, Chemla discloses the system and method wherein the state of all of the plurality of signals is sampled at the first IC whenever any of the signals change (Temperature sensor circuitry 62 of figure 5 monitors the temperature sensor of each device to see if it has reached a pre-determined threshold, COL. 4, lines 55 – 67).

As to claims 3 and 17, Chemla discloses the system and method, wherein the second IC drives each of the signals with new values received in-band each time that the plurality of signal values are received (The clock, reset, and TD1 signals of the second IC , processor 18, is driven to a new value, COL. 5, lines 50 – 55).

As to claims 4 and 18, Chemla discloses the system and method wherein the first IC comprises: signal logic associated with each of the plurality of signals (IC 60 comprises logic to affect change in the reset and clock circuitry for all related devices, figure 5, and , COL. 5, lines 60 – 65); and protocol logic, coupled to the signal logic associated with each of the signals, to transmit the signal values in-band to the protocol to the second IC (IC 60 comprises works within the protocol scheme of said devices, COL.5, lines 59 – 65).

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As to claim 15, Chemla discloses A system comprising: a chipset; an interface coupled to the chipset (Chipset comprises of clock generator 20, and IC 60 of figure 5); and

an integrated circuit (IC) coupled to the chipset, wherein the chipset simultaneously transmits the state of each of a plurality of signals not associated with the interface to the IC in-band via the interface each time that a change in the state of one of the plurality of signals is detected (IC, Processor 18 of figure 5 is coupled to chipset comprising clock generator 20 and IC 60. When an overheat condition is detected on ICs 50, output signal to the processor and other ICs that a temperature overheat situation has occurred, COL. 4, lines 55 – 67. This information is sent to the second IC , processor 18 of figure 5).

### ***Allowable Subject Matter***

The examiner objects to claims 5 – 10,13,14,19-24 as said claims are dependent upon a rejected base claim, but it would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

### ***Conclusion***


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher A. Daley whose telephone number is 571 272 3625. The examiner can normally be reached on 9 am. - 4p m.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571 272 3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

CAD  
10/31/05

  
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